TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

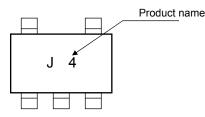
TC7SZ32F,TC7SZ32FU

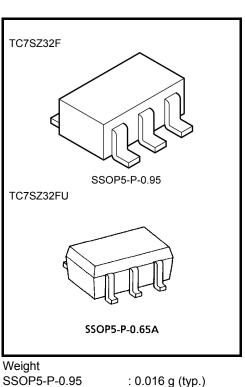
2 Input OR Gate

Features

- High output drive: ±24 mA (min) at V_{CC} = 3 V
- Super high speed operation: tpd=2.4 ns (typ.)
 - at V_{CC} = 5 V, 50 pF
- Operation voltage range: V_{CC (opr)} = 1.8~5.5 V
- 5.5-V tolerant inputs
- 5.5-V power down protection output
- Matches the performance of TC74LCX series when operated at 3.3- V V_{CC}

Marking





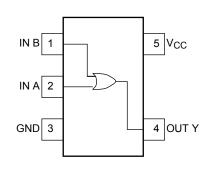
SSOP5-P-0.95 : SSOP5-P-0.65A :

: 0.006 g (typ.)

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Power supply voltage	V _{CC}	-0.5~6	V	
DC input voltage	V _{IN}	-0.5~6	V	
DC output voltage	V _{OUT}	-0.5~6	V	
Input diode current	I _{IK}	-20	mA	
Output diode current	IOK	-20	mA	
DC output current	IOUT	±50	mA	
DC V _{CC} /ground current	ICC	±50	mA	
Power dissipation	PD	200	mW	
Storage temperature	T _{stg}	-65~150	°C	
Lead temperature (10s)	ΤL	260	°C	

Pin Assignment (top view)



Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

<u>TOSHIBA</u>

Logic Diagram



Truth Table

Inp	out	Output
А	В	Y
L	L	L
L	Н	Н
Н	L	Н
Н	Н	Н

Operating Ranges

Characteristics	Symbol	Rating	Unit	
Supply voltage	V _{CC}	1.8~5.5	V	
Supply voltage		1.5~5.5 (Note 1)	v	
Input voltage	V _{IN}	0~5.5	V	
Output voltage	V _{OUT}	0~5.5 (Note 2)	V	
		0~V _{CC} (Note 3)	v	
Operating temperature	T _{opr}	-40~85	°C	
	dt/dv	0~20 (V_{CC} = 1.8 V, 2.5 V \pm 0.2 V)	ns/V	
Input rise and fall time		0~10 (V_{CC} = 3.3 V \pm 0.3 V)		
		0~5 (V _{CC} = 5.5 V \pm 0.5 V)		

Note 1: Data retention only

Note 2: $V_{CC} = 0 V$

Note 3: High or Low state

Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition			Ta = 25°C			Ta = -40~85°C		Unit		
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit		
High-level input			1.8	V _{CC} × 0.88	_	_	V _{CC} × 0.88	_	v	
voltage		2.3~5.5		V _{CC} × 0.75	_	_	V _{CC} × 0.75	_	v	
Low-level input VIL —	Ma				_	_	V _{CC} × 0.12	_	V _{CC} × 0.12	v
		2.3~5.5	_	_	V _{CC} × 0.25	_	V _{CC} × 0.25	V		
				1.8	1.7	1.8	_	1.7	_	-
			100 1	2.3	2.2	2.3		2.2		
			I _{OH} = –100 μA	3.0	2.9	3.0	_	2.9		
High-level	Maria	V _{IN} = V _{IH}		4.5	4.4	4.5	_	4.4		V
output voltage	VOH	or V _{IL}	I _{OH} = -8 mA	2.3	1.9	2.15	_	1.9	_	V
			I _{OH} = -16 mA	3.0	2.4	2.8	_	2.4		
			I _{OH} = -24 mA	3.0	2.3	2.68		2.3		
			I _{OH} = -32 mA	4.5	3.8	4.2	_	3.8		
			I _{OL} = 100 μA	1.8		0	0.1	_	0.1	0.1 0.1 0.1 0.3
				2.3	_	0	0.1	_	0.1	
				3.0	_	0	0.1	_	0.1	
Low-level	Max	V_{OL} $V_{IN} = V_{IL}$		4.5	_	0	0.1	_	0.1	
output voltage	VOL		I _{OL} = 8 mA	2.3		0.1	0.3		0.3	
			I _{OL} = 16 mA	3.0		0.15	0.4	_	0.4	
			I _{OL} = 24 mA	24 mA 3.0 — 0.2	0.22	0.55		0.55		
			I _{OL} = 32 mA	4.5		0.22	0.55	_	0.55	
Input leakage current	I _{IN}	V _{IN} = 5.5 V	V _{IN} = 5.5 V or GND		_	_	±1	_	±10	μA
Power off leakage current	IOFF	V_{IN} or $V_{OUT} = 5.5 V$		0.0	—	—	1	_	10	μA
Quiescent supply current	Icc	$V_{IN} = V_{CC}$	$V_{IN} = V_{CC}$ or GND		_	_	2	_	20	μA

AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3 \text{ ns}$)

Characteristics	Symbol	Test Condition		Ta = 25°C		Ta = −40~85°C		Unit	
Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Unit
Propagation delay time	^t pLH tpHL	$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.8	2.0	4.6	10.0	2.0	10.5	ns
			2.5 ± 0.2	0.8	3.0	7.0	0.8	7.5	
			$\textbf{3.3}\pm\textbf{0.3}$	0.5	2.4	4.7	0.5	5.0	
			5.0 ± 0.5	0.5	1.9	4.1	0.5	4.4	
		$C_L = 50 \text{ pF},$ $R_L = 500 \Omega$	$\textbf{3.3}\pm\textbf{0.3}$	1.5	3.0	5.2	1.5	5.5	
			5.0 ± 0.5	0.8	2.4	4.5	0.8	4.8	
Input capacitance	C _{IN}		0~5.5	_	4	_	_	_	pF
Power dissipation capacitance	6	(Note 4)	3.3		20		_	_	рF
	C _{PD}		5.5		26			_	

Note 4: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

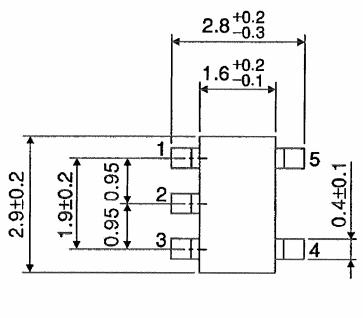
 $I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$

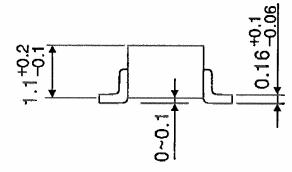
<u>TOSHIBA</u>

Package Dimensions

SSOP5-P-0.95

Unit : mm

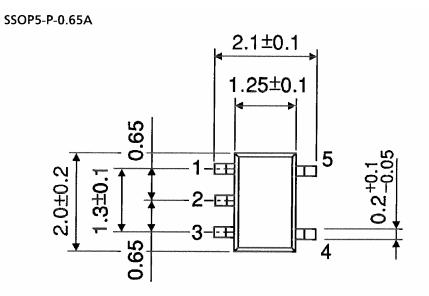


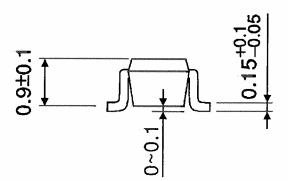


Weight: 0.016 g (typ.)

<u>TOSHIBA</u>

Package Dimensions





Weight: 0.006 g (typ.)

Unit : mm

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20070701-EN GENERAL

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